

SYSTEM AND METHOD FOR CLOCK PHASE RECOVERY**ABSTRACT OF THE DISCLOSURE**

[0045] A clock may be generated having a predetermined unit interval. Received data may then be compared to the virtual clock to determine whether more than one data change occurs during a particular unit interval. If more than one data change occurs during a particular unit interval, the virtual clock may not correspond to the source clock. The virtual clock may then be incremented by a phase shift offset and the data analyzed again. When a phase shifted virtual clock is identified that aligns with the data such that a plurality of data changes do not occur during a given unit interval, the embodiment may determine that a passing virtual clock may have been identified.